

Mitsubishi Q00/Q00UJ/Q01/QJ71

Supported Series: Mitsubishi Q series PLC with QJ71C24 communication module, Q00, Q00J, Q00UJ, Q01, Q02H, Q06H, Q12H, Q25H, Q12PH, Q25PH CPU port.

Website: <http://www.mitsubishi-automation.com>

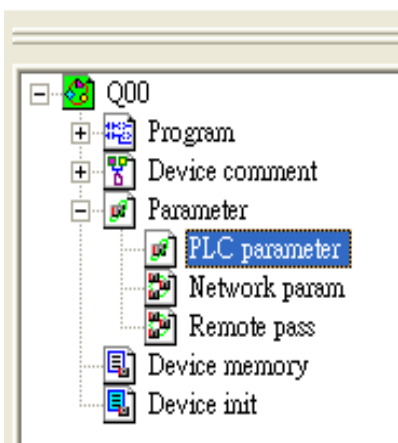
HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	Mitsubishi Q00/Q00UJ/Q01/QJ71		
PLC I/F	RS232	RS485 2W/4W, RS232	
Baud rate	9600	9600~115200	
Data bits	8		
Parity	Odd		
Stop bits	1		
PLC sta. no.	0		

Online simulator	Yes	Extend address mode	NO
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PLC Setting:

Q00, Q01 CPU port setting:



1. In GX Developer "PLC data list" click [PLC parameter].
2. In "PLC parameter" go to [Serial] page.
3. Select [Use serial communication].
4. Set [Transmission speed] to 9600~115200.
5. Select [Sum check].
6. Set [Transmission wait time] to 10ms.
7. Permit [RUN write setting].
8. Click [End] to close the dialog.
9. Write the PLC Parameter to PLC.
10. Reset PLC, the parameter will be activated.

Qn(H) Parameter

PLC name | PLC system | PLC file | PLC RAS | Device | Program
 Boot file | SFC | I/O assignment | Serial

☒ Use serial communication

Transmission speed
 19.2Kbps

☒ Sum check

Transmission wait time
 10ms

RUN write setting
☒ Permit

Data format value is fixed as below.
 Start bit :1 Parity bit:Odd
 Data bit:8 Stop bit:1

Acknowledge XY assignment | **Multiple CPU settings** | Default | Check | End | Cancel

QJ71 setting:

Q parameter setting

PLC name | PLC system | PLC file | PLC RAS | Device | Program | Boot file | SFC | I/O assignment

I/O Assignment(*)

Slot	Type	Model name	Points	StartXY
0	PLC			
1	0(*-0)	Intelli.	QJ71C24	32points
2	1(*-1)			
3	2(*-2)			
4	3(*-3)			
5	4(*-4)			
6	5(*-5)			
7	6(*-6)			

Assigning the I/O address is not necessary as the CPU does it automatically.
 Leaving this setting blank will not cause an error to occur.

Base setting(*)

Base model name	Power model name	Extension cable	Slots
Main			
Ext Base1			
Ext Base2			
Ext Base3			
Ext Base4			
Ext Base5			
Ext Base6			
Ext Base7			

Base mode
☒ Auto
☐ Detail

8 Slot Default
 12 Slot Default

(*)Settings should be set as same when using multiple CPU.

Import Multiple CPU Parameter | Read PLC data

Acknowledge XY assignment | **Multiple CPU settings** | Default | Check | End | Cancel

Module selection

Module selection

Module type: Serial Communication/Modem Interface Module

Module name: QJ71C24

QJ71C24N
 QJ71C24N-R2
 QJ71C24N-R4
QJ71C24
 QJ71C24-R2

OK | Cancel

Switch SettingNo set: QJ71C24

Item	CH1	CH2
Operation setting	Independence	Independence
Data Bit	8	7
Parity Bit	Exist	None
Odd/Even Parity	Odd	Odd
Stop Bit	1	1
Sum Check Code	Exist	None
Online Change	Enable	Disable
Change	Enable	Disable
Communication rate setting	9600bps	Automatic setting
Communication protocol setting	MC protocol (Type5)	Connecting GX Developer
Station number setting (0 to 31)	0	

OK | Cancel

Device Address:

Bit/Word	Device type	Format	Range	Memo
B	X	HHHH	0 ~ 1fff	Input Relay
B	Y	HHHH	0 ~ 1fff	Output Relay
B	M	DDDDD	0 ~ 61439	Internal Relay
B	L	DDDDD	0 ~ 32767	Latch Relay
B	F	DDDDD	0 ~ 32767	Annunciator
B	V	DDDDD	0 ~ 32767	Edge Relay
B	B	HHHH	0 ~ efff	Link Relay
B	TC	DDDD	0 ~ 2047	Timer Coil
B	SS	DDDDD	0 ~ 25471	Retentive Timer Contact
B	SC	DDDDD	0 ~ 25471	Retentive Timer Coil
B	CS	DDDDD	0 ~ 25471	Counter Contact
B	CC	DDDDD	0 ~ 25471	Counter Coil
B	SB	HHH	0 ~ 7ff	Special Link Relay
B	S	DDDD	0 ~ 8191	Step Relay
B	DX	HHHH	0 ~ 1fff	Direct Input
B	DY	HHHH	0 ~ 1fff	Direct Output
B	TS	DDDD	0 ~ 2047	Timer Contact
B	SM	DDDD	0 ~ 2047	
B	D_Bit	DDDDDDDDh	0 ~ 4212735f	
B	W_Bit	HHHHh	0 ~ 2ffff	
B	ZR_Bit	HHHHHh	0 ~ fe7fff	
B	ZR_Dec_Bit	DDDDDDDDh	0 ~ 1042431f	
W	W	HHHH	0 ~ 2fff	Link Register
W	TN	DDDD	0 ~ 2047	Timer Current Value
W	SN	DDDD	0 ~ 2047	Retentive Timer Current Value
W	CN	DDDD	0 ~ 1023	Counter Current Value
W	R	FFDDDDD	0 ~ 3132767	File Register (FF:File No. 0~31) (DDDDD:0~32767)
W	SW	HHH	0 ~ 7ff	Special Link Register
W	Z	DD	0 ~ 19	Index Register
W	ZR	HHHHH	0 ~ fe7a5	File Register
W	ZR_decimal_addr	DDDDDDD	0 ~ 1042341	
W	D	DDDDDDD	0 ~ 4212735	Data Register
W	SD	DDDD	0 ~ 2047	
W	Serial_No	D	0 ~ 7	
W	Product_No	D	0 ~ 7	

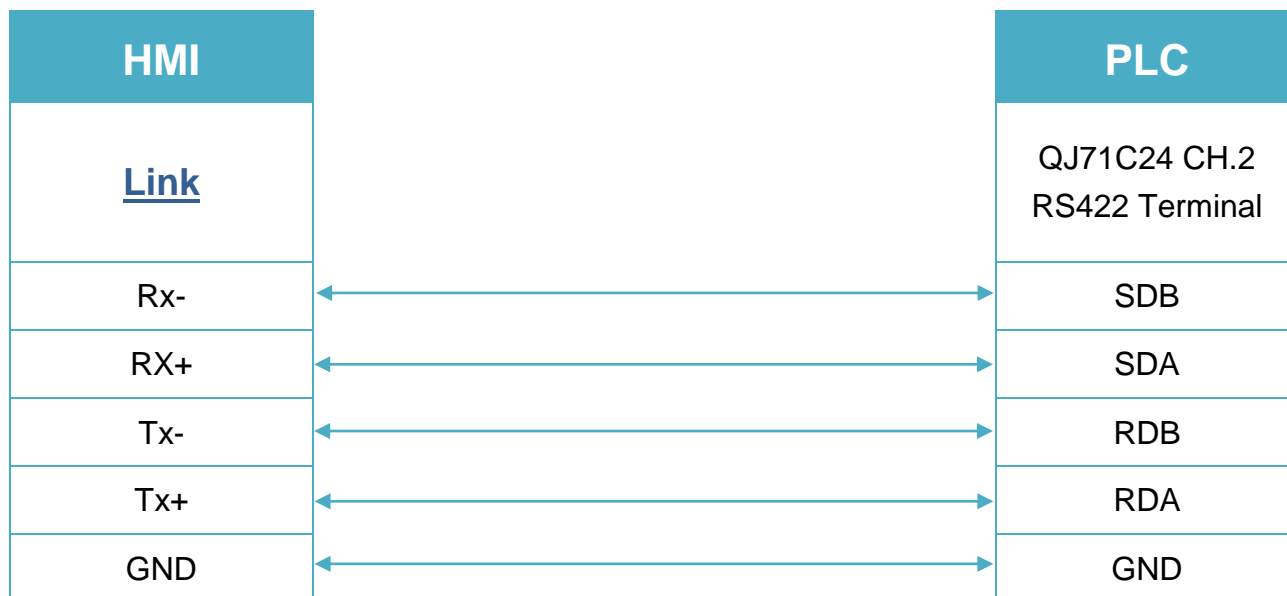
Wiring Diagram:

QJ71C24 CH.2 RS422 Terminal

Diagram 1

RS-485 4W

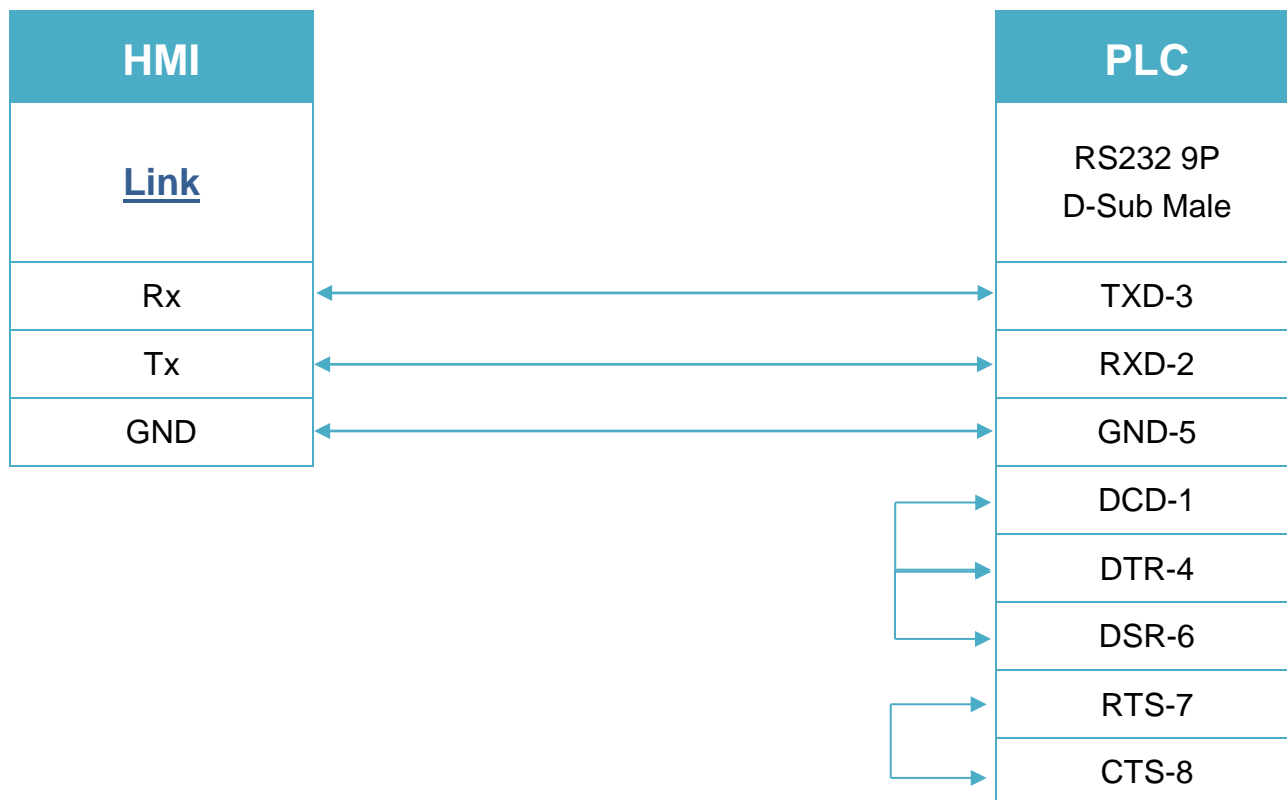
The serial port pin assignments may vary between HMI models, please click the following link for more information.



QJ71C24 CH.2 RS232

Diagram 2**RS-232**

The serial port pin assignments may vary between HMI models, please click the following link for more information.



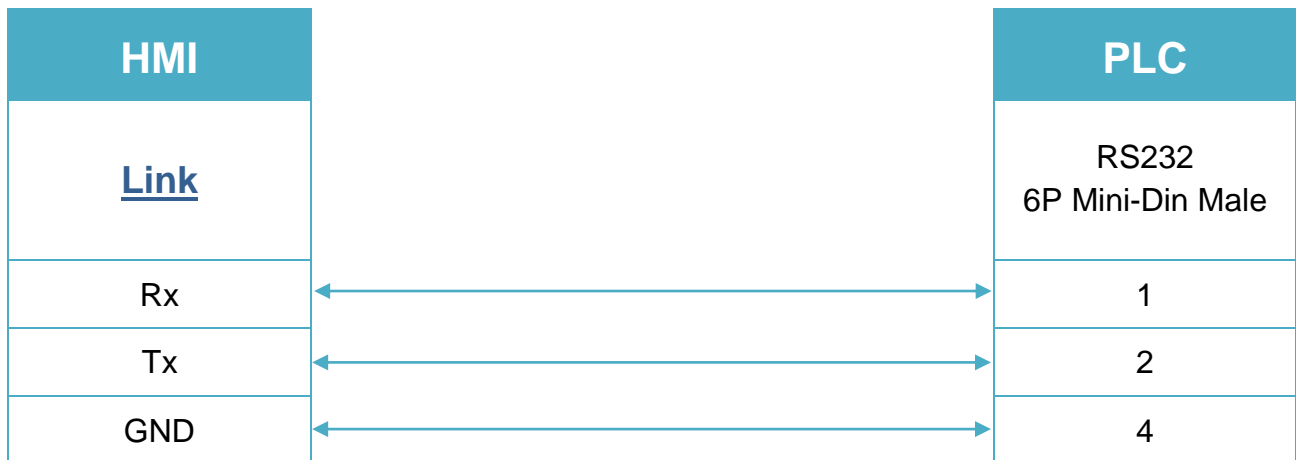
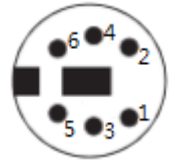
6P Mini-DIN: Q00, Q01 CPU port RS232

Diagram 3

RS-232

The serial port pin assignments may vary between HMI models, please click the following link for more information.

The following is the view from the soldering point of a connector.



6P Mini-DIN: Q00UJ CPU port RS232

Diagram 4

RS-232

The serial port pin assignments may vary between HMI models, please click the following link for more information.

The following is the view from the soldering point of a connector.

