

KEYENCE

KV-L20V/700/1000/3000/5000/7500/8000/Nano

Series

Supported series: KV-L20V, 700, 1000, 3000, 5000, 7500, 8000 series, KV Nano series

Website: <http://www.keyence.com/>

HMI Setting:

| Parameters | Recommended | Options | Notes |
|--------------|--|-------------------------|-------|
| PLC type | KEYENCE KV-L20V/700/1000/3000/5000/7500/8000/Nano Series | | |
| PLC I/F | RS232 | RS232,RS485 2W,RS485 4W | |
| Baud rate | 115200 | 9600 ~ 115200 | |
| Data bits | 8 | | |
| Parity | Even | | |
| Stop bits | 1 | | |
| PLC sta. no. | 0 | | |

| | | | |
|------------------|-----|---------------------|----|
| Online simulator | YES | Extend address mode | NO |
|------------------|-----|---------------------|----|

PLC Setting:

| | |
|--------------------|---------------------|
| Communication mode | KV mode (host link) |
|--------------------|---------------------|

Device Address:

| Bit/Word | Device type | Format | Range | Memo |
|----------|-------------|----------|-------------|---------------------------|
| B | MR | DDDDdd | 0 ~ 399915 | Intenal auxiliary relay |
| B | LR | DDDdd | 0 ~ 99915 | Latch relay |
| B | CR | DDDdd | 0 ~ 99915 | Control relay |
| B | R | DDDDdd | 0 ~ 199915 | Relay |
| B | B | HHHh | 0 ~ 7FFF | Link relay |
| B | T_Bit | DDDD | 0 ~ 9999 | Timer |
| B | C_Bit | DDDD | 0 ~ 9999 | Counter |
| B | DM_Bit | DDDDDDdd | 0 ~ 6553515 | Data memory bit |
| B | TM_Bit | DDDDdd | 0 ~ 999915 | Temporary data memory bit |
| B | CM_Bit | DDDDDDdd | 0 ~ 6553515 | Control memory bit |

| Bit/Word | Device type | Format | Range | Memo |
|----------|-------------|----------|--------------|-------------------------|
| B | EM_Bit | DDDDDDdd | 0 ~ 6553515 | Data memory |
| B | FM_Bit | DDDDDDdd | 0 ~ 52428715 | File register bit |
| B | CTC_Bit | D | 0 ~ 7 | |
| B | ZF_Bit | DDDDDDdd | 0 ~ 52428715 | File register (SQ) |
| B | W_Bit | HHHHh | 0 ~ 7FFFF | Link register bit |
| B | W_Bit_Dec | HHHHdd | 0 ~ 7FFF15 | |
| B | VM_Bit | DDDDDDdd | 0 ~ 6553515 | |
| B | VB_Bit | HHHHh | 0 ~ F9FF | |
| W | DM | DDDDD | 0 ~ 65535 | Data memory |
| W | TM | DDDD | 0 ~ 9999 | Temporary data memory |
| W | W | HHHH | 0 ~ 7FFF | Link register |
| W | VM | DDDDD | 0 ~ 65535 | |
| W | CM | DDDDD | 0 ~ 65535 | Control memory |
| W | EM | DDDDD | 0 ~ 65535 | Data memory |
| W | FM | DDDDDD | 0 ~ 524287 | File register |
| W | MR_Word | DDDD | 0 ~ 3999 | Intenal auxiliary relay |
| W | LR_Word | DDD | 0 ~ 999 | Latch relay |
| W | CR_Word | DDD | 0 ~ 999 | Control relay |
| W | ZF | DDDDDD | 0 ~ 524287 | File register (SQ) |
| W | VB | HHH | 0 ~ F9F | |
| W | R_Word | DDDD | 0 ~ 1999 | Relay |
| W | B_Word | HHH | 0 ~ 7FF | Link relay |
| W | T | DDDD | 0 ~ 9999 | Timer |
| W | C | DDDD | 0 ~ 9999 | Counter |
| W | T_Curr | DDDD | 0 ~ 9999 | Timer current |
| W | T_Preset | DDDD | 0 ~ 9999 | Timer preset |
| W | C_Curr | DDDD | 0 ~ 9999 | Counter current |
| W | C_Preset | DDDD | 0 ~ 9999 | Counter preset |
| W | TRM | D | 0 ~ 7 | |
| W | Z | DD | 1 ~ 12 | |
| W | CTH | D | 0 ~ 3 | |
| W | CTC | D | 0 ~ 7 | |

Wiring Diagram:

Diagram 1

RS-232 (OP-26486 RS232)

The serial port pin assignments may vary between HMI models, please click the following link for more information.

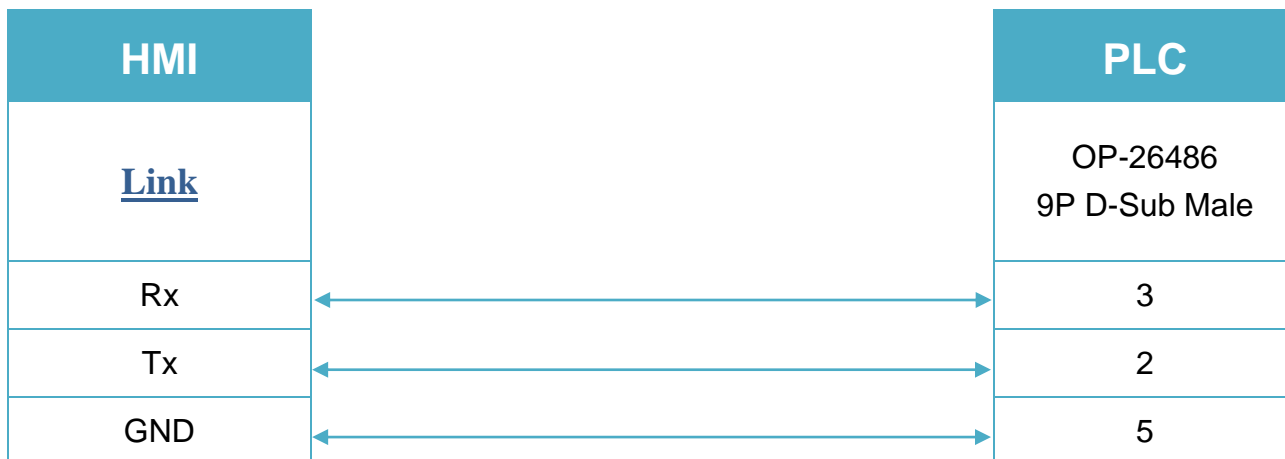


Diagram 2

RS-232 (KV-L20V Port2 RS232 terminal)

The serial port pin assignments may vary between HMI models, please click the following link for more information.



Diagram 3

RS-485 2W (KV-L20V Port2 RS485 2W)

The serial port pin assignments may vary between HMI models, please click the following link for more information.

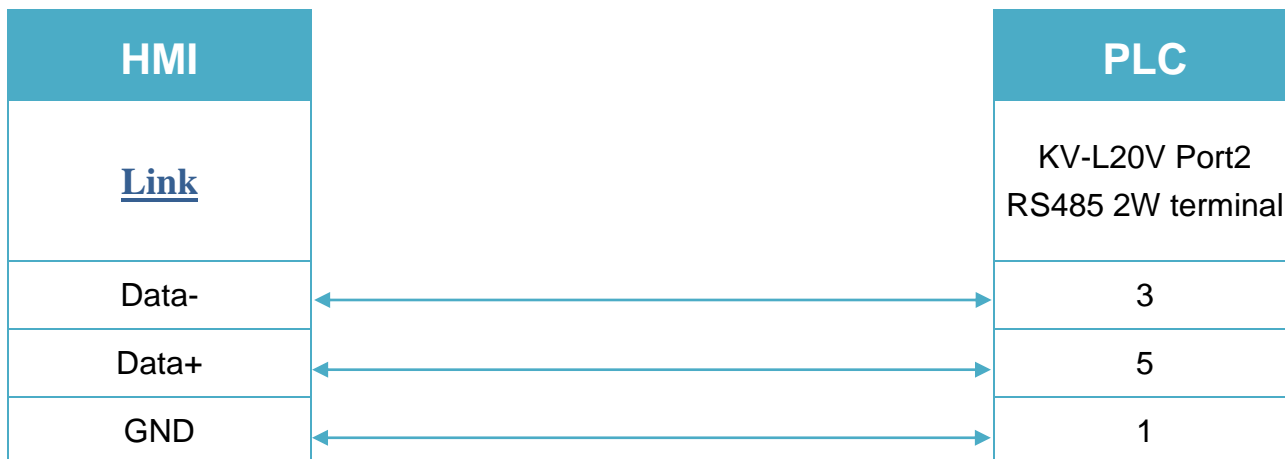


Diagram 4

RS-485 4W (KV-L20V Port2 RS485 4W terminal)

The serial port pin assignments may vary between HMI models, please click the following link for more information.

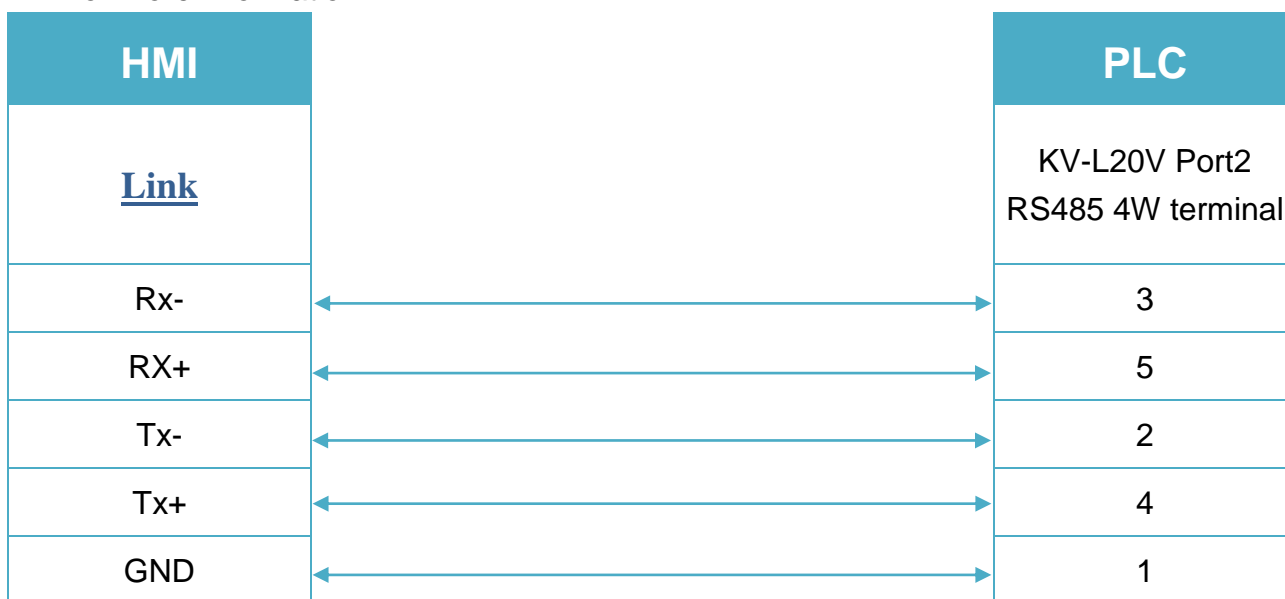


Diagram 5**RS-232 (RJ11 6P RS232)**