

LS XGI Fenet (Ethernet)

Supported Series: LS XGT series XGI CPU with XGL-EFMT 1thernet module.

Website: <http://www.lgis.com/>

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	LS XGI Fenet (Ethernet)		
PLC I/F	Ethernet		
Port no.	2004		
PLC sta. no.	0		

Device Address:

Bit/Word	Device type	Format	Range	Memo
B	AW_Bit	DDDDDDh	0 ~ 262143f	
B	IW_Bit	DDD.DD.Dh	0 ~ 127.15.3f	
B	QW_Bit	DDD.DD.Dh	0 ~ 127.15.3f	
B	MW_Bit	DDDDDDh	0 ~ 1048575f	
B	RW_Bit	DDDDh	0 ~ 32767f	
B	WW_Bit	DDDDDDh	0 ~ 524287f	
B	FW_Bit	DDDDh	0 ~ 9125f	
B	KW_Bit	DDDDh	0 ~ 65535f	
B	LW_Bit	DDDDh	0 ~ 11263f	
B	NW_Bit	DDDDh	0 ~ 25087f	
B	UW_Bit	DD.DD.DDh	0 ~ 31.15.31f	
B	AX	DDDDDDDD	0 ~ 4194303	
B	IX	DDD.DD.DD	0 ~ 127.15.63	
B	QX	DDD.DD.DD	0 ~ 127.15.63	
B	MX	DDDDDDDD	0 ~ 16777199	
B	RX	DDDDDD	0 ~ 524287	
B	WX	DDDDDDDD	0 ~ 8388607	
B	FX	DDDDDD	0 ~ 146015	
B	KX	DDDDDDDD	0 ~ 1048575	
B	LX	DDDDDDDD	0 ~ 1880223	
B	NX	DDDDDD	0 ~ 401407	
B	UX	DD.DD.DDD	0 ~ 31.15.511	

Bit/Word	Device type	Format	Range	Memo
W	AW	DDDDDD	0 ~ 262143	
W	IW	DDD.DD.D	0 ~ 127.15.3	
W	QW	DDD.DD.D	0 ~ 127.15.3	
W	MW	DDDDDD	0 ~ 1048575	
W	RW	DDDDD	0 ~ 32767	
W	WW	DDDDDD	0 ~ 524287	
W	FW	DDDDD	0 ~ 65535	
W	KW	DDDD	0 ~ 9125	
W	LW	DDDDD	0 ~ 11263	
W	NW	DDDDD	0 ~ 25087	
W	UW	DD.DD.DD	0 ~ 31.15.31	
DW	MD	DDDDDD	0 ~ 524288	
Byte	MB	DDDDDD	0 ~ 2097151	
LW	ML	DDDDDD	0 ~ 262143	

Wiring Diagram:

Ethernet cable:

