

Mitsubishi AJ71 (AnA/AnU CPU)

Supported Series: Mitsubishi AJ71 (AnA/AnU CPU)

Website: <http://www.mitsubishi-automation.com>

HMI Setting:

Parameters	Recommended	Options	Notes
PLC type	Mitsubishi AJ71 (AnA/AnU CPU)		
PLC I/F	RS485 4W	RS485 4W, RS232	
Baud rate	19200	9600, 19200	
Data bits	8	8	
Parity	Even	Even, Odd, None	
Stop bits	1	1	
PLC sta. no.	0		

PLC Setting:

Communication mode	Computer Link protocol 9600, Even, 8, 1 (default)
Mode setting switch	Format 1
Parity check	Enable
Sum check	Enable

Device Address:

Bit/Word	Device type	Format	Range	Memo
B	X	HHHH	0 ~ ffff	Input Bits
B	Y	HHHH	0 ~ ffff	Output Bits
B	M	DDDDD	0 ~ 65535	Internal Relays
B	T	DDDDD	0 ~ 65535	
B	C	DDDDD	0 ~ 65535	
B	B	HHHH	0 ~ ffff	
B	F	DDDDD	0 ~ 65535	
B	L	DDDDD	0 ~ 65535	
W	TV	DDDDD	0 ~ 65535	Timer Preset Value
W	CV	DDDDD	0 ~ 65535	Counter Preset Value
W	D	DDDDD	0 ~ 65535	Data Registers
W	W	HHHH	0 ~ ffff	
W	R	DDDDD	0 ~ 65535	

Wiring Diagram:

RS422 Terminal

Diagram 1

RS-485 4W

The serial port pin assignments may vary between HMI models, please click the following link for more information.

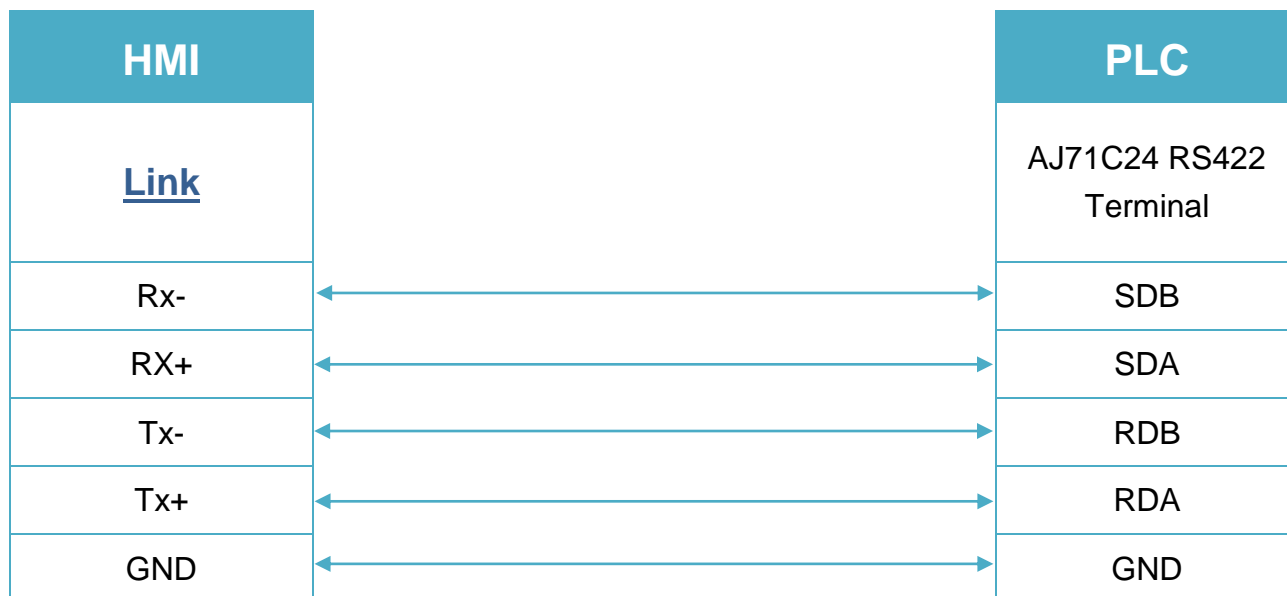


Diagram 2

RS-232

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